

APPENDIX B

VERSION WITH MARKINGS TO SHOW CHANGES MADE

37 C.F.R. § 1.121(b)(iii) AND (c)(ii)

CLAIMS:

1. (Amended) A current sense integrated circuit, comprising:

an amplifier circuit for receiving and amplifying a differential analog input signal at a first voltage level containing current sense information, wherein the amplifier circuit includes a circuit to minimize inherent temperature offset drift;

a pulse width modulator circuit for converting the differential analog input signal to a pulse width modulated signal at the first voltage level; and

a level shift circuit for converting the pulse width modulated signal from the first voltage level to a second voltage level[; and

a recovery circuit for reconstructing the analog input signal at the second voltage level].